

ANIL NEERUKONDA INSTITUTE OF TECHNOLOGY & SCIENCES
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

PROJECT TITLES
2016-17 Passed out Batch (XIII Batch)

Batch No.	Name of the Guide	Students Regd. Nos.	Title of the Project
1	Prof. G. Raja Rao	313126514014, 027, 004, 028, 034	Modeling and Simulation of Hybrid Wind / Photovoltaic Stand-Alone Generation System
2	Mr. S. Bala Murali	313126514016, 017, 045, 039	Speed Control of Induction Motor using PID and Sliding Mode Controller
3	Dr. J. Vijaya Kumar	313126514138, 139, 107, 080,101	Optimal Placement of PMU in a Transmission Network using Integer Linear Programming and Genetic Algorithm
		313126514122, 092, 124, 094, 100	Single-Phase Seven-Level Grid Connected Inverter for Photovoltaic System
4	Mr. Kottala Kiran Kumar	313126514018, 006, 005, 009	Simulation of 3-Phase Active Power Filter for Harmonic Reduction Due to Non-Linear Loads
5	Mrs. K.V.R. Swathi	313126514126, 073, 099, 130, 117	Bi-Directional Rotation of DC Motor with Remote Control by using Arduino
6	Mr. Kavala Kiran Kumar	31312651404, 056, 012, 046	Harmonic Mitigation by using Hysteresis Current Controlled Shunt Active Power Filter
7	Mr. B. Rajasekhar	313126514131, 137, 110, 114, 112	Simulation of a Three Level AC to DC Converter using Isolation Transformer
8	Mr. R. Satish	313126514037, 030, 055	Load Flow Analysis of Unbalanced Radial Distribution Networks with Distributed Generation
9	Mr. Ch.V.N. Raja	313126514049, 041, 019, 064	Stabilization of Linearized SMIB System by using GA based LQR Controller
10	Mr. L. Dinesh	313126514119, 128, 106, 084	Design of High Step-up Interleaved Converter with Voltage Multiplier Module
11	Mr. M. Sudheer Kumar	313126514103, 108, 091, 096, 312126514011	A New 3-Phase Multilevel Inverter for Medium and High Power Applications

12	Mr. K. Trinadh Babu	313126514060, 068, 036, 047	Design and Implementation of Firing Circuit Using Cosine control Scheme
13	Mr. L. Dinesh	312126514047, 039, 043, 026	Hardware Implementation of Single Input Multiple Output DC-DC Converter
14	Mr. S. Harish	313126514123, 082, 104, 311126514113	Performance of CUK Converter FED Brushless DC Motor
		313126514020, 003, 067, 026	Reduction of Power Electronic Components in Multi-Level Converters using New Switched Capacitor Diode
15	Mr. A. Dhanamjay	313126514081, 085, 089, 116	A Novel Distribution Automation using Intelligent Universal transformer(IUT)
16	Mr. L. Vijay	313126514057, 007, 001, 036	DC-DC Converter using Push-Pull Topology
17	Ms. A.S. Anitha Nair	313126514133, 143, 111, 127	Underground Cable Fault Distance Locator
18	Ms. D. Sri Lakshmi	313126514066, 048, 008, 065	Transformerless DC-DC Converters with High Step Up Voltage Gain
19	Mr. V. Rangavalli	313126514140, 095, 105, 083	Wireless Power Transmission using Class-E Power Amplifier from Solar Input
20	Mr. V. Anil Kumar	313126514052, 042, 022, 025	A New Multilevel Inverter Topology with A Cascaded H-Bridge and Minimum Number of Switches
21	Mr. N. Kiran	313126514090, 076, 087, 121	Identification of Faults in Transmission Lines using Wavelet Transform
22	Mr. J. Uday Venkatesh	313126514069, 038, 035, 058	Implementation of PV Fed Five Level Inverter with Pulse Width Modulation Control Scheme
23	Mr. R.V.L.N. Divakar	313126514115, 141, 144, 113	Enhancement of Power Quality by DC-Link Voltage Control
24	Mrs. T.V. Subhashini	313126514031, 051, 053, 070	Nonlinear Speed Control for PMSM System using Sliding Mode Control and Disturbance Compensation Technique
25	Mr. E. Appanna	313126514098, 074, 078, 118	Simulation of Grid Connected Photovoltaic System with MPPT
26	Mr. G. Yedukondalu	313126514021, 061, 044, 015	Improving Steady State Stability of Power System using STATCOM

27	Mr. B. Tulasi Rao	313126514062, 010, 029, 013	Simulation of SPWM Technique of Z-Source Inverter by using MATLAB
28	Mr. T. Subrahmanyam	313126514097, 132, 079, 077	A Single-Stage Micro-Inverter without using Electrolytic Capacitors
29	Mr. S.J.V. Prakash	313126514129, 086, 134, 088	Interleaved SEPIC Power Factor Pre-regulator using Coupled Inductors in discontinuous Conduction Mode with Wide Output Voltage
30	Mr. P. Naresh Kumar	313126514002, 054, 050, 033	Simulation Analysis of 100 KW Grid Connected Photovoltaic System
31	Mr. M.S.M. Divakar	313126514039, 032, 071, 011	Mitigation of Voltage SAG by using DVR
33	Mr. B. Satyanarayana	313126514120, 136, 125, 093	Contingency Analysis of Power System by using Active Power and Voltage Performance Index

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